

## **AMENDMENTS TO THE SPECIFICATION**

### **Please enter the following on page 1, line 19**

The subject matter of this application is a division under 37 C.F.R. §1.53(b) of and claims priority under 35 U.S.C. §121 to a co-pending non-provisional application by the same inventors as the instant application entitled, "Method and Apparatus for Electrostatically Aligning Integrated Circuits," having serial number 10/317,481, and filing date 12 December 2002 (Attorney Docket No. SUN-P7705-SPL). The claims in this divisional application were subject to a restriction requirement and are classified in class 438, subclass 401.